



DB3

BIDIRECTIONAL TRIGGER DIODE

Reverse Voltage 28- 32 Volts Power: 150mW

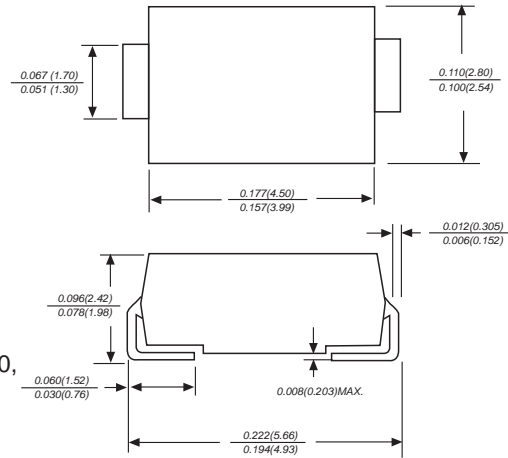
FEATURES

The plastic package
 VBO:28-36V version
 Low breakover current
 High temperature soldering guaranteed
 250°C/10 seconds,0.375" (9.5mm) lead length,
 5 lbs. (2.3kg) tension

MECHANICAL DATA

Case: JEDEC DO-214AC plastic body
Terminals: Plated axial leads, solderable per MIL-STD-750,
 Method 2026
Mounting Position: Any
Weight: DO-214AC 0.003 ounce, 0.093gram

DO-214AC



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

| | TEST CONDITION | SYMBOLS | VALUE | | | UNITS |
|--|------------------------------------|-------------------------------------|-------|------|------|-------|
| | | | Min. | Typ. | Max. | |
| Breakover voltage * | C=22nF ** | V _{BO} | 28 | 32 | 36 | VOLTS |
| Breakover voltage symmetry | C=22nF ** | +V _{BO} -I-V _{BO} | -3 | | 3 | VOLTS |
| Dynamic breakover voltage * | (NOTE 1) | ΔV ± I | 5 | | | VOLTS |
| Output voltage * | DIAGRAM2 | V _o | 5 | | | VOLTS |
| Breakover current * | C=22nF ** | I _{bo} | | | 100 | μA |
| Rise time * | DIAGRAM3 | t _r | | 1.5 | | μS |
| Leakage current * | V _R =0.5V _{BO} | I _B | | | 10 | μA |
| Power dissipation on printed circuit | T _A =65°C | P _d | | | 150 | mW |
| Repetitive peak on-state current | t _p =20μs f=100Hz | I _{TRM} | | | 2 | A |
| Thermal Resistances from Junction to ambient | | R _{θJA} | | | 400 | °C/W |
| Thermal Resistances from Junction to lead | | R _{θJL} | | | 150 | |
| Operating junction and storage temperature range | | T _J , T _{STG} | -40 | | 125 | °C |

* :Electrical characteristic appoicaboe in forward and reverse directions.

** :Connected in parallel with the devices.

Note 1: I_{bo} from I_{bo} to 10mA



RATINGS AND CHARACTERISTIC CURVES DB3

DIAGRAM 1: CURRENT-VOLTAGE CHARACTERISTICS

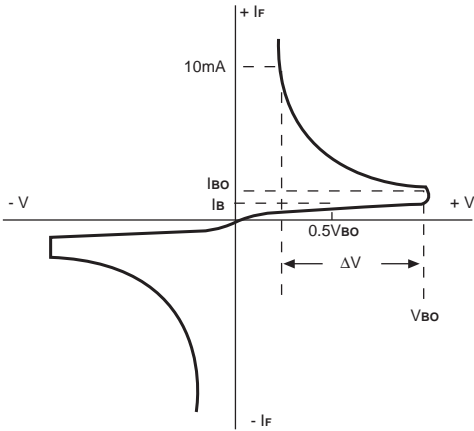


FIG. 1-POWER DISSIPATION VERSUS AMBIENT TEMPERATURE(MAXIMUM VALUES)

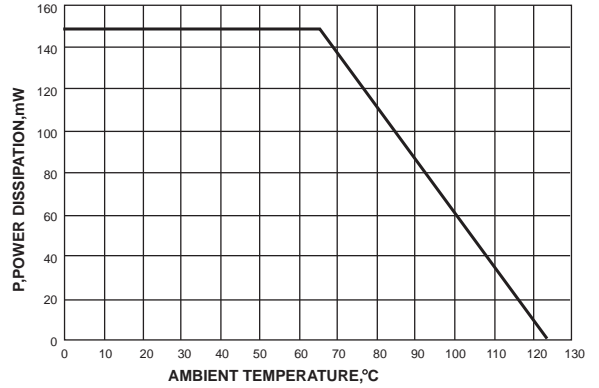


DIAGRAM 2: TEST CIRCUIT OUTPUT VOLTAGE

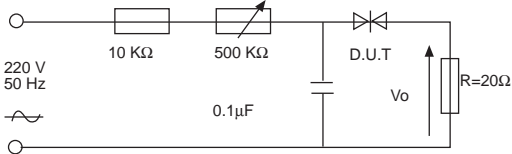


FIG. 2-PEAK PULSE CURRENT VERSUS PULSE DURATION (MAXIMUM VALUES)

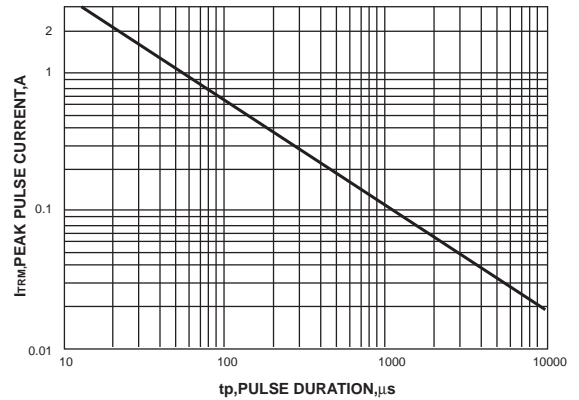


DIAGRAM 3: TEST CIRCUIT SEE DIAGRAM 2. ADJUST R FOR IP=0.5A

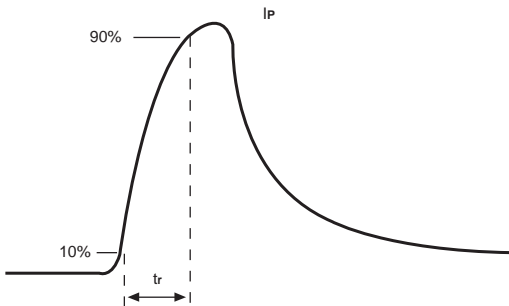


FIG. 3-RELATIVE VARIATION OF VBo VERSUS JUNCTION TEMPERATURE(TYPICAL VALUES)

